Table of Contents

ACM 25th International Conference on Supercomputing Conference and Workshop Organization ix
ICS 2011 Sponsor & Supporters xiv

Keynote Address
- Rethinking Shared-Memory Languages and Hardware
  Sarita V. Adve (University of Illinois at Urbana-Champaign) 1

Session 1: Best Paper Candidates
- An Execution Strategy and Optimized Runtime Support for Parallelizing Irregular Reductions on Modern GPUs
  Xin Huo, Vignesh T. Ravi, Wenjing Ma, Gagan Agrawal (The Ohio State University) 2
- Automatic Generation of Executable Communication Specifications from Parallel Applications
  Xing Wu, Frank Mueller (North Carolina State University), Scott Pakin (Los Alamos National Laboratory) 12
- Hystor: Making the Best Use of Solid State Drives in High Performance Storage Systems
  Feng Chen, David Koufaty (Intel Labs), Xiaodong Zhang (The Ohio State University) 22

Session 2: Transactional Memory
- Transactional Conflict Decoupling and Value Prediction
  Fuad Tabba (Oracle Corporation), Andrew W. Hay, James R. Goodman (The University of Auckland) 33
- Multiset Signatures for Transactional Memory
  Ricardo Quislant, Eladio Gutierrez, Oscar Plata, Emilio L. Zapata (University of Malaga) 43
- ZEBRA: A Data-Centric, Hybrid-Policy Hardware Transactional Memory Design
  Rubén Titos-Gil (Universidad de Murcia), Anurag Negi (Chalmers University of Technology),
  Manuel E. Acacio, José M. García (Universidad de Murcia), Per Stenström (Chalmers University of Technology) 53

Session 3a: Software Tools
- Scalable Fine-Grained Call Path Tracing
  Nathan R. Tallent, John Mellor-Crummey, Michael Franco (Rice University),
  Reed Landrum (Stanford University), Laksono Adhianto (Rice University) 63
- Generic Topology Mapping Strategies for Large-Scale Parallel Architectures
  Torsten Hoefler, Marc Snir (University of Illinois at Urbana-Champaign) 75

Session 3b: Non-volatile Memory Systems
- Page Placement in Hybrid Memory Systems
  Luiz E. Ramos (Rutgers University), Eugene Gorbunov (Intel Corporation),
  Ricardo Bianchini (Rutgers University) 85
- Performance Impact and Interplay of SSD Parallelism Through Advanced Commands, Allocation Strategy and Data Granularity
  Yang Hu (Huazhong University of Science and Technology & Wuhan National Laboratory for Optoelectronics),
  Hong Jiang (University of Nebraska-Lincoln),
  Dan Feng, Lei Tian (Huazhong University of Science and Technology & Wuhan National Laboratory for Optoelectronics and University of Nebraska-Lincoln),
  Hao Luo (Huazhong University of Science and Technology),
  Shuping Zhang (Beijing Institute of Computer Technology and Application) 96
Session 4a: Novel Hardware/Software Approaches

- **SecureME: A Hardware-Software Approach to Full System Security** ........................................ 108
  Siddhartha Chhabra, Brian Rogers, Yan Solihin (*North Carolina State University*), Milos Prvulovic (*Georgia Institute of Technology*)

- **Processing Data Streams with Hard Real-Time Constraints on Heterogeneous Systems** .......... 120
  Uri Verner, Assaf Schuster, Mark Silberstein (*Technion*)

Session 4b: Power

- **Coordinating Processor and Main Memory for Efficient Server Power Control** ............ 130
  Ming Chen, Xiaorui Wang, Xue Li (*University of Tennessee*)

- **Optimizing Throughput/Power Trade-offs in Hardware Transactional Memory Using DVFS and Intelligent Scheduling** ............................................................... 141
  Clay Hughes, Tao Li (*University of Florida*)

Keynote Address 2

- **Challenges and Opportunities in Renewable Energy and Energy Efficiency** ............ 151
  Steven Hammond (*National Renewable Energy Laboratory*)

Session 5: Performance and Resilience for Solver Algorithms

- **Characterizing the Impact of Soft Errors on Iterative Methods in Scientific Computing** ............................................................ 152
  Manu Shantharam, Sowmyalatha Srinivasamurthy, Padma Raghavan (*The Pennsylvania State University*)

- **High Performance Linpack Benchmark: A Fault Tolerant Implementation without Checkpointing** .......................................................... 162
  Teresa Davies, Christer Karlsson, Hui Liu, Chong Ding, Zizhong Chen (*Colorado School of Mines*)

- **Modeling the Performance of an Algebraic Multigrid Cycle on HPC Platforms** ........ 172
  Hormozd Gahvari (*University of Illinois at Urbana-Champaign*), Allison H. Baker, Martin Schulz, Ulrike Meier Yang (*Lawrence Livermore National Laboratory*), Kirk E. Jordan (*IBM T.J. Watson Research Center*), William Gropp (*University of Illinois at Urbana-Champaign*)

Session 6: Model-Based Techniques

- **Optimizing the Datacenter for Data-Centric Workloads** ............................................. 182
  Stijn Polfliet, Frederick Ryckbosch, Lieven Eeckhout (*Ghent University*)

- **Predictive Coordination of Multiple On-Chip Resources for Chip Multiprocessors** .......... 192
  Jian Chen, Lizy K. John (*The University of Texas at Austin*)

- **An Idiom-Finding Tool for Increasing Productivity of Accelerators** ....................... 202
  Laura Carrington (*UCSD/SDSC*), Mustafa M. Tikir (*Google Inc.*), Catherine Olschanowsky, Michael Laurenzano, Joshua Peraza, Allan Snively (*UCSD/SDSC*), Stephen Poole (*ORNL*)

Keynote Address 3

- **Performance Modeling as the Key to Extreme Scale Computing** .......................... 213
  William D. Gropp (*University of Illinois at Urbana-Champaign*)

Session 7: Programming Models

- **Mint: Realizing CUDA Performance in 3D Stencil Methods with Annotated C** ............ 214
  Didem Unat (*University of California, San Diego*), Xing Cai (*University of Oslo*), Scott B. Baden (*University of California, San Diego*)

- **MDR: Performance Model Driven Runtime for Heterogeneous Parallel Platforms** .......... 225
  Jacques A. Pielenaar, Anand Raghunathan (*Purdue University*), Srimat Chakradhar (*NEC Laboratories America*)

- **Active Pebbles: Parallel Programming for Data-Driven Applications** .................. 235
  Jeremiah J. Willcock (*Indiana University*), Torsten Hoeftler (*University of Illinois at Urbana-Champaign*), Nicholas Edmonds, Andrew Lumsdaine (*Indiana University*)
Session 8a: Accelerator-Based Mathematics

- **Automating GPU Computing in MATLAB** .................................................. 245
  Chun-Yu Shei, Pushkar Ratnalikar, Arun Chauhan (Indiana University)

- **Using GPUs to Compute Large Out-of-Card FFTs** .................................. 255
  Liang Gu, Jakob Siegel, Xiaoming Li (University of Delaware)

- **Automatic SIMD Vectorization of Fast Fourier Transforms**
  for the Larrabee and AVX Instruction Sets ........................................... 265
  Daniel S. McFarlin, Volodymyr Arbatov, Franz Franchetti (Carnegie Mellon University),
  Markus Puschel (ETH Zurich)

Session 8b: Caching

- **Cost-Effectively Offering Private Buffers in SoCs and CMPs** .................. 275
  Zhen Fang, Li Zhao, Ravi Iyer, Carlos Flores Fajardo, German Fabila Garcia (Intel Corp.),
  Seung Eun Lee (Seoul National University of Science and Technology),
  Bin Li, Steve King, Xiaowei Jiang, Srihari Makineni (Intel Corp.)

- **A Composite and Scalable Cache Coherence Protocol for Large Scale CMPs** .. 285
  Yi Xu, Yu Du, Youtao Zhang, Jun Yang (University of Pittsburgh)

- **Controlling Cache Utilization of HPC Applications** .............................. 295
  Swann Perarnau, Marc Tchiboukdjian, Guillaume Haard (Grenoble University)

Session 9a: Applications

- **Cosmic Microwave Background Map-Making at the Petascale and Beyond** .. 305
  Rajesh Sudarsan, Julian Borrill, Christopher Cantalupo, Theodore Kisner, Kamesh Madduri, Leonid Oliker,
  Yili Zheng, Horst Simon (Lawrence Berkeley National Lab)

- **A QHD-Capable Parallel H.264 Decoder** ........................................... 317
  Chi Ching Chi, Ben Jeurink (Technische Universität Berlin)

- **MP-PIPE: A Massively Parallel Protein-Protein Interaction Prediction Engine** 327
  A. Schoenrock, F. Dehne, J. R. Green, A. Golshani, S. Pilre (Carleton University)

Session 9b: Innovative Architecture Solutions

- **The Elephant and the Mice: The Role of Non-Strict Fine-Grain Synchronization**
  for Modern Many-Core Architectures .................................................... 338
  Juergen Ributzka, Yuhei Hayashi, Joseph B. Manzano, Guang R. Guo (University of Delaware)

- **F²BFLY: An On-Chip Free-Space Optical Network with Wavelength-Switching** .. 348
  Jin Ouyang, Chuan Yang, Dimin Niu, Yuan Xie, Zhiwen Liu (The Pennsylvania State University)

- **Karma: Scalable Deterministic Record-Replay** ...................................... 359
  Arkapava Basu (University of Wisconsin-Madison), Jayaram Bobba (Intel Corporation),
  Mark D. Hill (University of Wisconsin-Madison)

Session: Student Research Competition Posters & Posters

- **SRC: Information Retrieval as a Persistent Parallel Service on Supercomputer Infrastructure** 369
  Tobias Berka, Marian Vajtersic (University of Salzburg)

- **SRC: Damaris - Using Dedicated I/O Cores for Scalable Post-Petascale HPC Simulations** 370
  Matthieu Dorier (ENS Cachan Brittany - IRISA)

- **SRC: Fenixos - A Research Operating System Focused on High Scalability and Reliability** 371
  Stavros Passas, Sven Karlsson (Technical University of Denmark)

- **SRC: Soft Error Detection and Recovery for High Performance Linpack** 372
  Teresa Davies, Zizhong Chen (Colorado School of Mines)
• Poster: DVFS Management in Real-Processors ................................................................. 373
  Vasilis Spiliopoulos (Uppsala University), Georgios Keramidas (Industrial Systems Institute),
  Stefanos Kaxiras (Uppsala University), Konstantinos Efstathiou (University of Patras)

• SRC: OpenSHMEM Library Development ................................................................. 374
  Swaroop Pophale (University of Houston)

• SRC: Enabling Petascale Data Analysis for Scientific Applications
  Through Data Reorganization ..................................................................................... 375
  Yuan Tian (Auburn University)

• SRC: Virtual I/O Caching: Dynamic Storage Cache Management
  for Concurrent Workloads ....................................................................................... 376
  Michael R. Frasea, Ramya Prabhakar (The Pennsylvania State University)

• SRC: An Automatic Code Overlaying Technique for Multicores
  with Explicitly-Managed Memory Hierarchies .......................................................... 377
  Choonki Jang (Seoul National University)

• Poster: Programming Clusters of GPUs with OMPSs ............................................. 378
  Javier Bueno, Alejandro Dunin (Barcelona Supercomputing Center),
  Xavier Martorell, Eduard Ayguadé (Barcelona Supercomputing Center & Universitat Politécnica de Catalunya),
  Rosa M. Badia (Barcelona Supercomputing Center & Consejo Superior de Investigaciones Científicas),
  Jesús Labarta (Barcelona Supercomputing Center & Universitat Politécnica de Catalunya)

• Poster: Revisiting Virtual Channel Memory for Performance and Fairness
  on Multi-Core Architecture ..................................................................................... 379
  Licheng Chen, Yongbing Huang, Yungang Bao (Chinese Academy of Sciences),
  Onur Mutlu (Carnegie Mellon University), Guangming Tan, Mingyu Chen (Chinese Academy of Sciences)

• Poster: Implications of Merging Phases on Scalability of Multi-Core Architectures .... 380
  Madhavan Manivannan (Chalmers University of Technology), Ben Juurlink (Technische Universität Berlin),
  Per Stenstrom (Chalmers University of Technology)

• SRC: Facilitating Efficient Parallelization of Information Storage
  and Retrieval on Large Data Sets ........................................................................... 381
  Steven Feldman (University of Central Florida)

• SRC: Automatic Extraction of SST/macros Skeleton Models .................................. 382
  Amruth Rudraiah Dakshinamurthy (University of Central Florida)

Author Index ............................................................................................................. 383