Hardware and Software Synthesis, Optimization, and Verification from Esterel

Manipulation Algorithms for K*BMDs

Combining Partial Order and Symmetry Reductions

Partial Model Checking with ROBDDs

Space Efficient Reachability Analysis Through Use of Pseudo-Root States

The Reference Component of PEP

A Tool to Support Formal Reasoning about Computer Languages

The Term Processor Generator Kimwitu

Graphs in METAFrame: The Unifying Power of Polymorphism

A Tableau System for Linear-TIME Temporal Logic

Model-Checking for a Subclass of Event Structures

Real-Time Logics: Fictitious Clock as an Abstraction of Dense Time

MOSEL: A Flexible Toolset for Monadic Second-Order Logic

A Brief Introduction to Coloured Petri Nets

Design/CPN - A Computer Tool for Coloured Petri Nets

Formal Verification of Statecharts with Instantaneous Chain Reactions

Compositional State Space Generation from Lotos Programs

Syntactic Detection of Process Divergence and Non-local Choice in Message Sequence Charts

An Automata Based Verification Environment for Mobile Processes

Compositional Performance Analysis

Incremental Development of Deadlock-Free Communicating Systems

Automatic Synthesis of Specifications from the Dynamic Observation of Reactive Programs

Visual Verification of Reactive Systems

Theorem Prover Support for the Refinement of Stream Processing Functions

Integration in PVS: Tables, Types, and Model Checking

Test Generation for Intelligent Networks Using Model Checking

Mechanically Verified Self-Stabilizing Hierarchical Algorithms

The Bounded Retransmission Protocol Must Be on Time!

Author Index