Message from the General Chair
Message from the Technical Program Co-Chairs
Conference Committee
Pros and Cons of Public Domain VLSIC Design Suites p. 3
A Multilevel Approach to Teaching Hardware Description Languages p. 5
A VLSI Circuit Design Course for Practitioners and Researcher p. 9
Experience Extending VLSI Design with Mathematical Logic p. 11
Integrating CAE Tools into Computer Engineering Courses p. 13
Cache Memory Design for Embedded Systems Based on Program Locality Analysis p. 16
Semiconductor Teaching Chips p. 19
An Electronics Manufacturing Minor in Engineering with Emphasis on Rapid Prototyping p. 21
Network-Based Simulation Laboratories for Microelectronics Systems Design and Education p. 23
Personal-Computer Based Digital and Analog VLSI Design Laboratories p. 25
Integrating Mixed Signal IC Design Research into a Project-Based Undergraduate Microelectronics Curriculum p. 28
[[Learning by Virtual Doing ]]: Protocol Simulators for Surface Analysis in Microelectronics p. 30
EDA on UNIX/Sparc and Win95/Intel Platforms: Does Compatibility Exist? p. 32
The University of Tennessee/Oak Ridge National Laboratory Joint Program in Mixed-Signal VLSI an Monolithic Sensors p. 34
A Systems Design Course Emphasizing Interfaces p. 36
Meeting the Computer Competency Expectations of the Construction Industry p. 39
Diverse-Projects Design-Experiences in Analog/Digital Microelectronic Systems p. 43
Microelectronic Systems Education at an Urban Non-Residential Campus p. 45
Interacting with Physical Devices over the Web p. 47
A World Wide Web Education Center for Analog Microsystems Design Education p. 49
Microelectronic Design Cooperative Education Program p. 51
An Industrial-Strength Design Flow in Just Fifteen Easy Weeks! p. 53
Toward an Optimized Computer Assisted Electronics Laboratory p. 55
System Oriented VLSI Curriculum at KTH p. 57
ProTest: A Low Cost Rapid Prototyping and Test System for ASICs and FPGAs p. 60
An Undergraduate Advanced Computer Design Course Using Virtual-Prototyping p. 62
Experiences Teaching Design Automation in the Introductory Level Course p. 64
The 21st Century Engineering Consortium p. 66
Low Cost, Prototype ASIC and MCM Fabrication and Assembly from the MOSIS Service p. 68
Industrial Feedback for a Microelectronics Curriculum p. 70
A Simplified Module Interface Style for Synthesis Education p. 73
Interactive Learning Toolbox for Logic Synthesis with VHDL p. 77
Teaching the Design of a Chip Under the Cadence Opus Environment Using the Alliance Cells Libraries
EUROPRACTICE and FUSE: The European Commission Programs for Supporting Education and Technology Transfer in Microelectronics
Experiences Teaching Synthesis of FPGAs and Testable ASICs
A Project Oriented Undergraduate CMOS Analog Microelectronic System Design Course
Introducing Multimedia in Teaching of Digital System Design
Microelectronic Systems Design Educational Challenge
Curricular Integration for Next Generation in Microsystems Design Education
Infrastructure for Laboratory Distribution
Experience Teaching a Senior Level Course on Digital Design Using FPGAs
A WWW Facilitated Rapid System Prototyping Class
Distributed Learning via the World Wide Web through Interactive Modules
Artificial Device: A New Way of Using Monte Carlo Simulations for Pedagogic Applications
Industrial Strength Design Automation Tools in an Introductory Computer Engineering Laboratory
A Distance Laboratory for Computer-Aided Design
Role of FPGAs in Undergraduate Project Courses
Using Hypermedia for Programmable Logic Devices Education
A World-Wide-Web Based Instrumentation Pool Real Testing in a Virtual World
Implementation of the VLSI Education Program at South Dakota State University
The French Microelectronics Training Network Supported by Industry and Education Ministries
Project-Oriented Training of Engineering Students on MMIC and HMIC at LHOG/INPG
Including HDL and Synthesis in the EE and CSE Digital Curriculum
Multimedia Optimisation and Demonstration for Education in Microelectronics (MODEM): A New European Microelectronics Telematics Based Educational Initiative
Continuing Education for Small and Medium-Sized Industries: The French JESSICA Programme
Using Synthesis, Simulation, and Hardware Emulation to Prototype a Pipelined RISC Computer System
The Teaching of VHDL in Computer Architecture
Getting Started with VHDL
Flip-chip Assembly for Senior Designs in the 21st Century
Facilitating Interconnect-Based VLSI Design
Doing More with Less: Magic on Windows 95/NT
Significant Microelectronics Systems Design Experience for a Heterogeneous Class of CS, CE, and EE Students
A Multidisciplinary Course in Rapid Prototyping of Wearable Computers
Educational Use of MOSIS
Mixing Web Technologies and Educational Concepts to Promote Quality of Training in ASIC CAD