Message from the Program Chair

Steering Committee

Technical Committee

Invited Talk: Hardware/Software Co-Verification in ATM

A Methodology for Simulation and Synthesis of Mixed Hardware/Software Systems (talk only)

Instruction Set Definition and Instruction Selection for ASIPs

Data Routing: A Paradigm for Efficient Data-Path Synthesis and Code Generation

Timing Analysis for Synthesis in Microprocessor Interface Design

Applications of Attributed-Behavior Synthesis

Computing Lower Bounds on Functional Units before Scheduling

Timing Estimation for Behavioral Descriptions

Efficient Timing Constraint Derivation for Optimal Retiming High Speed Processing Units

SMASH: A Program for Scheduling Memory-Intensive Application-Specific Hardware

Ensemble Representation and Techniques for Exact Control-Dependent Scheduling

Panel Session: Is High-Level Synthesis Marketable?

Invited Talk: State-of-the-Art Compiler Optimization

An Integrated Approach to Retargetable Code Generation

Bit-Alignment for Retargetable Code Generators

Code Generation for a DSP Processor

Retargetable Assembly Code Generation by Bootstrapping

CodeSyn: A Retargetable Code Synthesis System (talk only)

Concurrent Testing in High-Level Synthesis

Testing Two-Phase Transition Signaling Based Self-Timed Circuits in a Synthesis Environment

A Hybrid Numeric/Symbolic Program for Checking Functional and Timing Compatibility of Synthesized Designs

A Divide-and-Conquer Approach for Asynchronous Interface Synthesis

Rapid Prototyping of Fault-Tolerant VLSI Systems

Panel Session: ASICs vs ASIPs

Specification of Interface Components for Synchronous Data Paths

Global Node Reduction of Linear Systems Using Ratio Analysis

A Specification Invariant Technique for Operation Cost Minimisation in Flow-Graphs

Controller and Datapath Trade-Offs in Hierarchical RT-Level Synthesis

How Datapath Allocation Affects Controller Delay

An Algorithm for the Allocation of Functional Units from Realistic RT Component Libraries

Author Index

Table of Contents provided by Blackwell's Book Services and R.R. Bowker. Used with permission.