High Resolution MUSE-NTSC Converter

A PALplus Compatible HDTV Transmission System

HDTV Video Decoder Which Can Be Implemented with Low Complexity

An NTSC to HDTV Video Conversion System by Using the Block Processing Concept

Edge and Motion Controlled Spatial Upconversion

A New Scanning Line Conversion Technique on Frame Basis

Development of an Experimental Full-Digital HDTV System: Algorithm and Implementation

An Experimental Digital VCR for Consumer Use

SDVCR: A High Data Rate Versatile Recorder

Data-Placement Procedure for Multi-Speed Digital VCR

Two Layered DCT Based Coding Scheme for a Digital HD-VCR

An Image Compression Scheme for Digital Video Cassette Recording

A Study on Detection of Blocks for Digital VCR

Strategies for Multi-Rate Recording for a Consumer Digital VCR

Data Selection Strategies for Digital VCR Long Play Mode

An Advanced Multi-Standard TV Video/Sound IF

A New Single Reference VIF/SIF-PLL for Multistandard TV-Signal Demodulation

A Video and Sound IF Processing IC with Automatic Tuning Loops

Low Peripheral NTSC TV-Processor

Bus Controlled Multistandard Video Processor

One Chip TV Picture Booster

A Low Cost, Monolithic Color Picture-in-Picture Device

User Interface Concepts for TV Set-Top Units

Developing a User Interface for Interactive Television

Client-Server Paradigm for Interactive TV

Standardization of Set Top Box Design for Interactive Television

An Individually Addressable TV Receiver With Interactive Channel Guide Display, VCR, and Cable Box Control

A High Performance Video Server for Karaoke Systems

A Power Efficient Method for Information Reporting in Infrared Remote Controllers

Pyramidal Multiresolution Source Coding for HDTV

Image Compression Using Hybrid Neural Networks

FPGA Implementation of the LRU Algorithm for Video Compression

Design and Hardware Construction of a High Speed and Memory Efficient Huffman Decoding

A 54MHz Motion Estimation Engine for Real-Time MPEG Video Encoding

A DBS Digital Television System Using MPEG Compression and High Power Transponders

New System "W-VHS" VCR for Multi-Purpose Recording Including HDTV

A Small Size Deck Mechanism and Recording Technology for Digital VCRs

Analysis of VCR Tape Positioning Accuracy with Chassis Deformation
Equalizer Adaptation with a Geometrical Criterion and Fuzzy Logic
Combined Smart Power IC for VCR with PWM Controlled Motor Voltage
High Density Video Disc for HDTV Baseband Signal
Digital TBC and DOC System for a Laser Disk Player Using an 1H Dual Port Line Memory

HDTV Serial Interface System
Scalable Compression for Image Browsing
Ready to Use Protocol Software for the "Home Systems" Specification
A Remote Electronic Object Emulation System for Home Bus Applications
Whole House Control Utilizing a True Computer Network
System Architecture and Interface for an Apartment Management System
Visual Communication System in an Apartment House Using Only Twisted Paired Cable

The Challenges of Designing a User Interface for Consumer Interactive Television
A New Hierarchical Approach for Recognition of Unconstrained Handwritten Numerals

Neural Network Generalization Using Error Correcting Codes
A Mandarin Speech Dictation System Based on Neural Network and Language Processing Model
State Problems in Programming Human-Controlled Devices
User Recognition of Standard A/V Control Pictograms
Video on Demand Visual Interface for Interactive TV
A "Clickless" Multi-Channel Volume Control I. C. with Serial Bus
A High Efficiency Power Amplifier for Car Audio
Karaoke CD Grows to Video CD
SC Realization of Antialiasing CCIR 601 Video Filters
CAM-based VLSI Architectures for Dynamic Huffman Coding
VLSI Architecture for a Low Complexity Algorithm for Intraframe Sub-Band Coding of HDTV Images
The Magnetoresistive Sensor and its Applications in Emerging Technologies
Fuzzy Logic at Work in Real Products
The Television as Robot Servant

A New Buffer Control Strategy for Image Data Compression
A Quantization Control for MPEG2 Compatible Image Quality Improvement
Quantity Control for JPEG Image Data Compression Using Fuzzy Logic Algorithm
A Fast Vector Quantization Encoder for Video Coding
Hierarchical Delta Modulation for Compression of Surveillance Video Systems
MPEG2 Video Codec Using Image Compression DSP
Parallel Implementation of Transform-Based DCT Filter-Bank for Video Communications
Low-Cost Digital Electronic Still Cameras for Computer Imaging
A Single-Chip CCD Signal Processor for Digital Still Cameras
The Trustworthy Digital Camera: Restoring Credibility to the Photographic Image