Message from the Programme Chairs
ACSD 2001 Organisation
Programme Committee
Reviewers
Invited Talks
From Code to Models
Making Meaningful Models for Mere Mortals
Overcoming Heterophobia: Modeling Concurrency in Heterogeneous Systems
Performance Issues
Correct Performance of Transaction Capabilities
Analysis of Performance Limitations in Multithreaded Multiprocessor Architectures
Abstracting from Failure Probabilities
Asynchronous Communication
Algorithms for Signal and Message Asynchronous Communication Mechanisms and Their Analysis
Refinement Issues
Property Preserving Transition Refinement with Concurrent Runs: An Example
Implementing Communicating Processes in the Event of Interface Difference
Object-Oriented Approach
Codesign and Embedded Systems
A Petri Net Meta-Model to Develop Software Components for Embedded Systems
Strength and weaknesses of Genetic List Scheduling for Heterogeneous Systems
Software Implementation of Synchronous Programs
Embedding Imperative Synchronous Languages in Interactive Theorem Provers
Asynchronous Circuits
A Structural Encoding Technique for the Synthesis of Asynchronous Circuits
Optimization of Asynchronous Delay-Insensitive Pipeline Latency Using Stage Reorganization and Optimal Stage Parameter Estimation
Towards Synthesis of Monotonic Asynchronous Circuits from Signal Transition Graphs
Semi-Hiding Operators and the Analysis of Active-Edge Specifications for Digital Circuits
Verification and Testing
Exploration Testing
Exploiting Stabilizers and Parallelism in State Space Generation with the Symmetry Method
Automata Generation for On-the-Fly Automatic Verification Using Formulas of an Interval Logic
The BDD Space Complexity of Different Forms of Concurrency
Petri Net Synthesis
Synthesis of Net Systems with Inhibitor Arcs from Step Transition Systems